Electronic Acknowledgement Receipt					
EFS ID:	2871572				
Application Number:	10776101				
International Application Number:					
Confirmation Number:	2835				
Title of Invention:	HIGH SPEED POWER-GATING TECHNIQUE FOR INTEGRATED CIRCUIT DEVICES INCORPORATING A SLEEP MODE OF OPERATION				
First Named Inventor/Applicant Name:	Kim C. Hardee				
Customer Number: 25235 Filer: William J. Kubida/Julie Lange					
		Filer Authorized By:	William J. Kubida		
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File Listing:	
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Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)			
1		DOC060.PDF	33213 22d0c79c32eb7oe/82tb247d1b119849 st1516e7	yes	4			

	Multipart Description/PDF files in .zip description				
	Document Description	Start	End		
	Letter to Official Draftsman Filed P/E	1	1		
	Drawings-only black and white line drawings	2	4		
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/IDO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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